

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No. 10/624,628
Filing Date July 21, 2003
Inventor Luan C. Tran
Assignee Micron Technology, Inc.
Group Art Unit 2811
Examiner Unknown
Attorney Docket No. MI22-2356
Title: Methods of Forming Semiconductor Constructions

Commissioner for Patents
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Dated: April 16, 2004

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Although it is believed that no fees are due, the Commissioner is hereby authorized to charge any fees under 37 C.F.R. 1.16 and 1.17 which may be required by this paper to Deposit Account No. 23-0925.

Dated: April 16, 2004

By: Jennifer J. Taylor
Jennifer J. Taylor, Ph.D.
Reg. No. 48,711

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT
 References -See Attached Form PTO-1449

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The attached form PTO-1449 is submitted in compliance with 37 C.F.R. **OFFICIAL**
 § 1.56. Copies of the cited art are included with the exception of U.S. patents
 and published U.S. applications (1276 Off. Gaz. Pat. Off. 55, 05 August 2003).
 No admission is made regarding whether all the submitted references are prior
 art.

This Supplemental Information Disclosure Statement is being filed within
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Respectfully submitted,

Dated:

April 16, 2004

By:

Jennifer J. Taylor
 Jennifer J. Taylor, Ph.D.
 Reg. No. 48,711

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10924628LIST OF ART CITED BY APPLICANT
(Use several sheets if necessary)APPLICANT
Tsun C. TranFILING DATE
July 21, 2003GROUP
2811

U.S. PATENT DOCUMENTS

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	5,841,185	11-1998	Ishikawa			
	AB	6,331,458 B1	12-2001	Anjum et al.			
	AC	6,251,744 B1	06-2001	Su et al.			
	AD						
	AE						
	AF						
	AG						
	AH						
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FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AM							
	AN							
	AO							
	AP							
	AQ							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

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EXAMINER

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